

(19) KOREAN INTELLECTUAL PROPERTY OFFICE

KOREAN PATENT ABSTRACTS

(11)Publication

number:

100278653 B1

(43)Date of publication of application:

21.10.2000

(21)Application number: 1019980002052

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(22)Date of filing: 23.01.1998

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(51)Int. Cl

G11C 11/407

(54) DDR MODE SEMICONDUCTOR MEMORY DEVICE

(57) Abstract:



PURPOSE: A DDR(Double Data Rate) mode semiconductor memory device is provided to select one of a DDR mode and an SDR(Single Data Rate) mode by forming a DDR mode semiconductor memory device having an SDR mode input/output function.

CONSTITUTION: The first memory core(10) stores a plurality of datum and outputs the addressed data by a low address and the first column address to the first data line. The second memory core(20) stores a plurality of datum and outputs the addressed data by the low address and the first column address to the first memory core(10) and the second data line. A control signal generation portion(32) generates the first clock having a period of integer times, the second clock having a twofold period of the first clock, and outputs one of the first and the second clocks as an internal clock. An amplification and multiplexing circuit(30) is synchronized with the internal clock. The amplification and multiplexing circuit(30) receives data from the first and the second memory cores(10,20) and amplifies and multiplexes the received data.

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Legal Status

Date of final disposal of an application (20000925)

Patent registration number (1002786530000)

Date of registration (20001021)

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